

Electronic Acknowledgement Receipt

EFS ID:	3525006
Application Number:	10518779
International Application Number:	
Confirmation Number:	6609
Title of Invention:	Insulated-gate semiconductor device and approach involving junction-induced intermediate region
First Named Inventor/Applicant Name:	Kailash Gopalakrishnan
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Filer Authorized By:	Robert J. Crawford
Attorney Docket Number:	STFD.035US
Receipt Date:	26-JUN-2008
Filing Date:	06-JUN-2005
Time Stamp:	15:50:06
Application Type:	U.S. National Stage under 35 USC 371

Payment information:

Submitted with Payment	no
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1	Amendment - After Non-Final Rejection	STFD035US_OAR.pdf	782759 174b42b545b00430d0009ac5291dc62 aaf507b74	no	15

Warnings:

Information:

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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office

If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.